

Vijay Madiseti Ph.D.
Curriculum Vitae
<http://users.ece.gatech.edu/~vkm/>

Professional Summary



Dr. Madiseti is a full Professor in Electrical & Computer Engineering at Georgia Tech, having joined in 1989 and is a Fellow of the IEEE. He has served as consultant to MIT Lincoln Labs and John Hopkins University Applied Physics Labs (APL) on programs related to Software, Computer Architectures and Wireless Networks. He has also founded two companies, VP Technologies, Inc. in 1995, and Soft Networks, LLC offering products in the areas of semiconductor ICs, and software for multimedia and wireless applications, respectively. He has led one of the largest embedded computing & software programs in the country since 1999 as part of Georgia Tech's \$75M Yamacraw Program (which has now evolved to Georgia Electronic Design Center), and has directed or co-directed large research programs that include DARPA's Rapid Prototyping of Application Specific Signal Processors (1994-1997), and the US Air Force's Electronic Parts Obsolescence Initiative (1998-2001).

Expertise

- Audio, Speech & Video Technology
- Broadcast Television & Cable Networks
- Computers & Mobile Phones
- Digital Signal Processing (DSP)
- Multimedia
- Networking (Wired/Wireless) & Internet Technologies & Protocols/Standards
- Semiconductors & IC Design
- Software, including: Web 2.0; VHDL; Image Processing; Transport Protocols
- Telecommunications & Wireless/Cellular Technologies & Architectures
- Video, TV & Image Processing

Education

<u>Year</u>	<u>College or University</u>	<u>Degree</u>
1989	University of California, Berkeley	Ph.D., Electrical Engineering & Computer Sciences
1984	Indian Institute of Technology (IIT) – Kharagpur, India	B. Tech (Hons), Electronics & Electrical Comm. Engineering

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Professional Experience

From: 2007
To: Present
Organization: Elastic Video, Inc.
Title: Director
Summary: Startup commercialized through Georgia Tech's VentureLab development image and video processing software for high-resolution medical electronics market.

From: 2001
To: Present
Organization: Soft Networks, LLC
Title: Director
Summary: Technology for Software Development for Wireless & Multimedia Markets. A startup commercialized through Georgia Tech support focusing on embedded software development tools and compilers for electronics products.

From: 1995
To: Present
Organization: VP Technologies, Inc.
Title: Director
Summary: Startup commercialized through Georgia Tech's Advanced Technology Development Corporation (ATDC) focusing on digital IC development for military market.

From: 1989
To: Present
Organization: Georgia Tech, Atlanta – School of ECE
Title: Professor of Electrical & Computer Engineering
Summary: Professor in Digital Signal Processing, Computer Engineering. His areas of research including computer software, digital signal processing & telecommunications, computer architecture & systems design, multimedia – video, imaging and speech/audio, systems engineering, and semiconductors.

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From: 1984
To: 1989
Organization: University of California, Berkeley
Title: Post Graduate Researcher

Consulting Projects

Consulting Engagement:

Client: Johns Hopkins University – Applied Physics Laboratory
Services Provided: Wireless Networks. Video & Multi-Sensor Localization Targets
Date: 2005 – 2007

Consulting Engagement:

Client: MIT Lincoln Labs
Services Provided: Area of Radar Signal Processing & Rapid Prototyping
Date: 1995 – 1996

Consulting Engagement:

Client: Wireless telecom integrator
Services Provided: Area of Speech and Audio Codecs Software development (through Soft Networks, LLC)
Date: Undisclosed

Consulting Engagement:

Client: Large semiconductor manufacturer
Services Provided: Area of TV set top boxes & video processing (through Soft Networks, LLC)
Date: Undisclosed

Litigation Support Experience

Dr. Madiseti has served as an expert testifying or consulting witness in more than a dozen high profile patent/IP litigation cases. In the past year, he has testified at trial in three ITC 337 Patent/IP Litigation proceedings, has presented talks at professional meetings on the subjects of "Impact of KSR v. Teleflex on Obviousness" and "Corporate Strategies for Patent Litigation." Dr. Madiseti has been engaged as a testifying Expert Witness on numerous patent infringement matters, working for the Nation's most prestigious law firms and top national and international companies.

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Professional Affiliations, Achievements & Awards

Dr. Madiseti has authored or co-authored several books, including VLSI Digital Signal Processors (IEEE Press, 1995), The Digital Signal Processing Handbook (CRC Press, 1998), Quick Turn-Around ASIC Design in VHDL (Springer, 1996), and Platform-Centric System on Chip Design (Springer, 2005), and has served as an Editor or Guest Editor of several IEEE magazines and journals, including IEEE Trans, On Circuits & Systems II (1993-1995), IEEE Micro, and IEEE Design & Test of Computers. He is a Fellow of the IEEE, and recently received the 2006 Frederick Emmons Terman Medal from the American Society of Engineering Education (ASEE) for contributions to Electrical & Computer Engineering.

- Fellow, IEEE
- Recipient, American Society of Engineering Education (ASEE) *Frederick Emmons Terman* Medal & Award (2006)
- Recipient, Georgia Tech Outstanding Doctoral Dissertation Advisor Award (2001)
- Recipient, VHDL International Best PhD Dissertation Advisor Award (1997)
- Technical Program Chair, IEEE MASCOTS (1994)
- Recipient, IBM Faculty Development Award (1991)
- Recipient, NSF Research Initiation Award (1991)
- Recipient, NSF RI Award (1990)
- Recipient, IBM Faculty Development Award (1990)
- Technical Program Chair, IEEE Workshop on Parallel and Distributed Simulation (1990)
- Recipient, Demetri Angelakos Outstanding Graduate Student Award at U.C. Berkeley (1989)
- Recipient, IEEE Ira Kay Best Paper Award (1989)
- Recipient, Ira Kay IEEE/ACM Best Paper Award for Best Paper presented at IEEE Annual Simulation Symposium (1989)
- Recipient, General Proficiency Prize, Indian Institute of Technology, Kharagpur (1984)
- Recipient, Jagasdis Bose National Science Talent Fellowship, Indian Institute of Technology, Kharagpur (1980-1984)

Books and Publications

Books

1. The Digital Signal Processing Handbook (Section Edition): Vol 1 (*Fundamentals*), Vol 2 (*Video, Speech & Associated Standards*), Vol 3 (*Wireless, Networking & Sensor Processing*), CRC Press, 2009 (To be published). Editor-in-Chief: *Madiseti, V. K.*
2. Platform-Centric Approach to System-on-Chip (SoC) Design. *Madiseti, V. K., Arpnikanondt, A.* Boston: MA, Springer, 2004, 280 pp.
3. VHDL: Electronics Systems Design Methodologies. *Madiseti, V. K. (Editor)* Boston: MA, IEEE Standards Press, 2000, ISBN 0-7381-1878-8.
4. The Digital Signal Processing Handbook *Madiseti, V. K., Williams, D. (Editors)* Boca Raton, Fla, 1998, 2500 pp.
5. Quick-Turnaround ASIC Design in VHDL *Romdhane, M., Madiseti, V.K., Hines, J.* Boston: MA, Kluwer Academic Press, 1996, 190 pp.
6. VLSI Digital Signal Processors *Madiseti, V. K.;* Boston: MA, IEEE Press: Butterworth Heinemann, 1995, 525 pp.

Edited Books & Collection of Papers:

1. Modeling, Analysis, Simulation of Computer & Telecommunications Systems *Madiseti, V., Gelenbe, E., Walrand, J. W. (Editors)* Los Alamitos: CA, IEEE Computer Society Press, 1994, 425 pp.
2. Advances in Parallel & Distributed Simulation *Madiseti, V. K.; Nicol, D., Fujimoto, R. (Editors)* San Diego, CA: SCS Press, 1991, 200 pp.
3. Modeling & Simulations on Microcomputers *Madiseti, V. K. (Editor)* San Diego, CA: SCS Press, 138 pp. 1990.

Editorship of Journals & Transactions:

1. International Journal in VLSI Signal Processing Editorial Board: 1995 - Present
2. IEEE Design & Test of Computers Special Issue: Reengineering Digital Systems April – June 1999 (Vol 16, No 2) *Madiseti, V. K (Editor)* Los Alamitos: CA, IEEE Computer Society Press, 1999.
3. IEEE Design & Test of Computers Special Issue: Rapid Prototyping of Digital Systems Fall 1996 (Vol 13, No 3) *Madiseti, V., Richards, M. (Editors)* Los Alamitos: CA, IEEE Computer Society

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Press, 1994, 425 pp.

4. IEEE Transactions on Circuits & Systems II *Associate Editor*: 1993-1995.
5. International Journal in Computer Simulation *Associate Editor*: 1990-1993

Refereed Journal Publications:

1. "Space-Time Codes for Wireless & Mobile Applications", M. Sinnokrot, V. Madiseti, The Digital Signal Processing Handbook, CRC Press, 2009 (to be published)
2. "Technologies for Internet Video Search", A. Bahga and V. Madiseti, The Digital Signal Processing Handbook, CRC Press, 2009 (to be published)
3. "Embedded Alamouti Space-Time Codes for High Rate and Low Decoding Complexity," M. Sinnokrot, V. Madiseti, and J. Barry, *Asilomar Conference on Signals, Systems, and Computers*, ([Asilomar 2008](#)), Pacific Grove, California, October 26-29, 2008.
4. "The Design of an End-to-End Handoff Management Protocol," A. Argyriou, Madiseti, V. K., *Wireless Networks*, 13(1): 61-75, 2007
5. "Computationally Efficient SNR Estimation for Bandlimited WCDMA Systems," L. Jatunov, Madiseti, V. K., *IEEE Transactions on Wireless Communications*, Volume 5, Issue 13, December 2006, Pages 3480-3491.
6. "Playback Adaptation with Transport Protocol Awareness for Wireless Video Streaming, A. Argyriou and V. Madiseti, *Proc. IEEE Globecom* 2006.
7. "A Soft-Handoff Transport Protocol for Media Flows in Heterogeneous Mobile Networks," A. Argyriou, Madiseti, V. K., *Computer Networks*, Vol 50, Issue 11, Pages 1860-1871, August 2006.
8. "Electronic System, Platform & Package Codesign," Madiseti, V. K., *IEEE Design & Test of Computers*, Volume 23, Issue 3, June 2006. Pages: 220-233.
9. A Novel End-to-End Approach for H.264 Video Streaming Over the Internet, Argyriou, A.; Madiseti, V. K.; *Kluwer Telecommunications Systems*, Vol. 28, No. 2, Pages 133-150, Jan 2005. Special Issue on Multimedia Streaming.
10. Using a new protocol to enhance path reliability and load balancing in mobile ad hoc networks Argyriou, A.; Madiseti, V.K.; *Journal of Ad Hoc Networks*, Elsevier Press, pp. 60-74, 2006
11. Closed-form for infinite sum in bandlimited CDMA *Jatunov, L.A.; Madiseti, V.K.*; *Communications Letters, IEEE* , Volume: 8 , Issue: 3 , March 2004 Pages:138 – 140
12. On fast algorithms for computing the inverse modified discrete cosine transform *Yun-Hui Fan; Madiseti, V.K.; Mersereau, R.M.*; *Signal Processing Letters, IEEE* , Volume: 6 , Issue: 3 , March 1999 Pages:61 – 64

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13. System on chip or system on package? *Tummala, R. R.; Madiseti, V. K.*; Design & Test of Computers, IEEE ,Volume: 16 , Issue: 2 , April-June 1999 Pages:48 – 56
14. Reengineering legacy embedded systems *Madiseti, V.K.; Jung, Y.-K.; Khan, M.H.; Kim, J.; Finnissy, T.*; Design & Test of Computers, IEEE ,Volume: 16 , Issue: 2 , April-June 1999 Pages:38 – 47
15. Reengineering digital systems *Madiseti, V. K.*; Design & Test of Computers, IEEE ,Volume: 16 , Issue: 2 April-June 1999 Pages:15 – 16
16. Parameter optimization of robust low-bit-rate video coders *Sangyoun Lee; Madiseti, V.K.*; Circuits and Systems for Video Technology, IEEE Transactions on, Volume: 9 Issue: 6 , Sept. 1999 Pages:849 – 855.
17. Adaptive mobility management in wireless networks *Jeongwook Kim; Madiseti, V.K.*; Electronics Letters Volume: 34 , Issue: 15 , 23 July 1998 Pages:1453 – 1455
18. Efficient implementation of two-band PR-QMF filterbanks *Hezar, R.; Madiseti, V. K.*; Signal Processing Letters, IEEE ,Volume: 5 , Issue: 4 , April 1998 Pages:92 – 94
19. Interface design for core-based systems *Madiseti, V.K.; Lan Shen*; Design & Test of Computers, IEEE, Volume: 14 , Issue: 4 , Oct.-Dec. 1997 Pages:42 – 51
20. Incorporating cost modeling in embedded-system design *Debardelaben, J.A.; Madiseti, V.K.; Gadiant, A.J.*; Design & Test of Computers, IEEE ,Volume: 14 , Issue: 3 , July-Sept. 1997 Pages:24 – 35
21. On homomorphic deconvolution of bandpass signals *Marenco, A. L.; Madiseti, V. K.*; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] Volume: 45 , Issue: 10 , Oct. 1997 Pages:2499 – 2514
22. A case study in the development of multi-media educational material: the VHDL interactive tutorial *Gadiant, A.J.; Stinson, J.A., Jr.; Taylor, T.C.; Aylor, J.H.; Klenke, R.H.; Salinas, M. H.; Madiseti, V. K.; Egolf, T.; Famorzadeh, S.; Karns, L. N.; Carter, H. W.*; Education, IEEE Transactions on ,Volume: 40 , Issue: 4 , Nov. 1997 Pages:17 pp.
23. Rapid digital system prototyping: current practice, future challenges *Madiseti, V. K.*; Design & Test of Computers, IEEE ,Volume: 13 , Issue: 3 , Fall 1996 Pages:12 – 22
24. Conceptual prototyping of scalable embedded DSP systems *Dung, L. -R.; Madiseti, V. K.*; Design & Test of Computers, IEEE ,Volume: 13 , Issue: 3 , Fall 1996 Pages: 54 – 65
25. Advances in rapid prototyping of digital systems *Madiseti, V.K.; Richards, M.A.*; Design & Test of Computers, IEEE ,Volume: 13 , Issue: 3 , Fall 1996 Pages: 9
26. Combined modulation and error correction codes for storage channels *Jaejin Lee; Madiseti, V. K.*; Magnetics, IEEE Transactions on ,Volume: 32 , Issue: 2 , March 1996 Pages:509 – 514

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27. Model-based architectural design and verification of scalable embedded DSP systems-a RASSP approach *Dung, L. -R.; Madiseti, V. K.; Hines, J. W.*; Chapter in VLSI Signal Processing, IX, 1996. Pages:147 – 156
 28. Low-power digital filter implementations using ternary coefficients *Hezar, R.; Madiseti, V. K.*; Chapter in VLSI Signal Processing, IX, 1996., Pages:179 – 188
 29. All-digital oversampled front-end sensors *Romdhane, M. S.B.; Madiseti, V. K.*; Signal Processing Letters, IEEE, Volume: 3 , Issue: 2 , Feb. 1996 Pages:38 – 39
 30. Modeling COTS components in VHDL *Calhoun, S., Reese, R; Egolf, T., Madiseti, V.K.*; Journal of VLSI Signal Processing, Volume: 14 , Issue: 2 , Nov 1996 Pages: 24 – 31
 31. VHDL-Based Rapid Systems Prototyping *Egolf, T.; Madiseti, V.K.*; Journal of VLSI Signal Processing, Volume: 14 , Issue: 2 , Nov 1996 Pages: 40-52
 32. System partitioning of MCMs for low power *Khan, S.A.; Madiseti, V.K.*; Design & Test of Computers, IEEE ,Volume: 12 , Issue: 1 , Spring 1995 Pages:41 – 52
 33. Error correcting run-length limited codes for magnetic recording *Jaejin Lee; Madiseti, V. K.*; Magnetics, IEEE Transactions on ,Volume: 31 , Issue: 6 , Nov. 1995 Pages:3084 – 3086
 34. Virtual prototyping of embedded microcontroller-based DSP systems *Madiseti, V. K.; Egolf, T. W.*; Micro, IEEE ,Volume: 15 , Issue: 5 , Oct. 1995 Pages:9 – 21
 35. Constrained multitrack RLL codes for the storage channel *Lee, J.; Madiseti, V.K.*; Magnetics, IEEE Transactions on ,Volume: 31 , Issue: 3 , May 1995 Pages:2355 – 2364
 36. LMSGEN: a prototyping environment for programmable adaptive digital filters in VLSI *Romdhane, M. S.B.; Madiseti, V. K.*; Chapter in VLSI Signal Processing, VII, 1994., Pages:33 – 42
 37. Fixed-point co-design in DSP *Egolf, T.W.; Famorzadeh, S.; Madiseti, V.K.*; Chapter in VLSI Signal Processing, VII, 1994., Pages:113 – 126
 38. A fast spotlight-mode synthetic aperture radar imaging system *Madiseti, V. K.*; Communications, IEEE Transactions on ,Volume: 42 , Issue: 234 , February-April 1994 Pages:873 – 876
 39. Rapid prototyping on the Georgia Tech digital signal multiprocessor *Curtis, B.A.; Madiseti, V.K.*; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] ,Volume: 42 , Issue: 3 , March 1994 Pages:649 – 662
 40. Low-power signaling in asymmetric noisy channels via spectral shaping *Sipitca, M.; Madiseti, V.K.*; Signal Processing Letters, IEEE, Volume: 1 , Issue: 8 , Aug 1994 Pages:117 – 118
 41. A quantitative methodology for rapid prototyping and high-level synthesis of signal processing algorithms *Madiseti, V.K.; Curtis, B.A.*; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] ,Volume: 42 , Issue: 11 , Nov. 1994 Pages:3188 – 3208

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42. Computer Simulation of Application-Specific Signal Processing Systems *Casinovi, G.; Madiseti, V.K.*; International Journal in Computer Simulation, Vol. 4, No. 4, Nov 1994.
 43. The fast discrete Radon transform. I. Theory *Kelley, B. T.; Madiseti, V. K.*; Image Processing, IEEE Transactions on , Volume: 2 , Issue: 3 , July 1993 Pages:382 – 400
 44. The Georgia Tech digital signal multiprocessor *Barnwell, T.P., III; Madiseti, V.K.; McGrath, S.J.A.*; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 41 , Issue: 7 , July 1993 Pages:2471 – 2487
 45. The MIMDIX Environment for Parallel Simulation *Madiseti, V. K.; Hardaker, D.; Fujimoto, R. M.*; Journal of Parallel and Distributed Computing, v18, no. 4, August 1993, Pages: 473-83.
 46. Efficient VLSI Architectures for the Arithmetic Fourier Transform (AFT) *Kelley, B. T.; Madiseti, V. K.*; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 41 , Issue: 1 , January 1993 Pages:365-378
 47. Synchronization mechanisms for distributed event-driven Computation; *Madiseti, V. K.; Hardaker, D.* : ACM Transactions on Modeling and Computer Simulation, v 2, n 1, Jan. 1992, Pages: 12-51
 48. Seismic migration algorithms on parallel computers *Madiseti, V. K.; Messerschmitt, D. G.*; Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 39 , Issue: 7 , July 1991 Pages:1642 – 1654
 49. Asynchronous algorithms for the parallel simulation of event-driven dynamical systems *Madiseti, V. K.; Walrand, J. C.; Messerschmitt, D. G.* : ACM Transactions on Modeling and Computer Simulation, v 1, n 3, July 1991, Pages: 244-74
 50. Multilevel range/NEXT performance in digital subscriber loops *Brand, G.; Madiseti, V.; Messerschmitt, D.G.*; Communications, Speech and Vision, IEE Proceedings I [see also IEE Proceedings-Communications] , Volume: 136 , Issue: 2 , April 1989 Pages:169 – 174
 51. [Note: During the period 2001-2003, our focus was on generating IP for the Yamacraw program through submission of patents and invention disclosures – See Provisional Patents & Patents sections].

Peer Reviewed Conference Publications:

1. Realizing load-balancing in ad-hoc networks with a transport layer protocol *Argyriou, A.; Madiseti, V.*; Wireless Communications and Networking Conference, 2004. WCNC. 2004 IEEE Volume: 3 , 21-25 March 2004 Pages:1897 - 1902 Vol.3
2. Streaming H.264/AVC video over the Internet *Argyriou, A.; Madiseti, V.*; Consumer Communications and Networking Conference, 2004. CCNC 2004. First IEEE , 5-8 Jan. 2004 Pages:169 – 174
3. Enforcing interdependencies and executing transactions atomically over autonomous mobile data stores using SyD link technology *Prasad, S.K.; Bourgeois, A.G.; Dogdu, E.; Sunderraman, R.; Yi*

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- Pan; Navathe, S.; Madiseti, V.;* Distributed Computing Systems Workshops, 2003. Proceedings. 23rd International Conference on , 19-22 May 2003 Pages:803 – 809
4. Performance evaluation and optimization of SCTP in wireless ad-hoc networks *Argyriou, A.; Madiseti, V.;* Local Computer Networks, 2003. LCN '03. Proceedings. 28th Annual IEEE International Conference on , 20-24 Oct. 2003 Pages:317 - 318
 5. Implementation of a calendar application based on SyD coordination links *Prasad, S.K.; Bourgeois, A.G.; Dogdu, E.; Sunderraman, R.; Yi Pan; Navathe, S.; Madiseti, V.;* Parallel and Distributed Processing Symposium, 2003. Proceedings. International, 22-26 April 2003 Pages:8 pp.
 6. Bandwidth aggregation with SCTP *Argyriou, A.; Madiseti, V.;* Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE Volume: 7 , 1-5 Dec. 2003 Pages:3716 - 3721 vol.7
 7. Software streaming via block streaming *Kuacharoen, P.; Mooney, V.J.; Madiseti, V. K.;* Design, Automation and Test in Europe Conference and Exhibition, 2003 , 2003 Pages:912 – 917
 8. Frequency-dependent space-interleaving for MIMO OFDM systems *Mohajerani, P.; Madiseti, V.K.;* Radio and Wireless Conference, 2003. RAWCON '03. Proceedings , Aug. 10-13, 2003 Pages:79 - 82
 9. A media streaming protocol for heterogeneous wireless networks *Argyriou, A.; Madiseti, V.;* Computer Communications, 2003. CCW 2003. Proceedings. 2003 IEEE 18th Annual Workshop on , 20-21 Oct. 2003 Pages:30 – 33
 10. Effect of handoff delay on the system performance of TDMA cellular systems *Turkboylari, M.; Madiseti, V.K.;* Mobile and Wireless Communications Network, 2002. 4th International Workshop on , 9-11 Sept. 2002 Pages:411 – 415
 11. A debugger RTOS for embedded systems *Akgul, T.; Kuacharoen, P.; Mooney, V.J.; Madiseti, V. K.;* Euromicro Conference, 2001. Proceedings. 27th , 4-6 Sept. 2001 Pages:264 - 269
 12. Adaptability, extensibility and flexibility in real-time operating systems *Kuacharoen, P.; Akgul, T.; Mooney, V.J.; Madiseti, V. K.;* Digital Systems, Design, 2001. Proceedings. Euromicro Symposium on , 4-6 Sept. 2001 Pages:400 – 405
 13. System design and re-engineering through virtual prototyping: a temporal model-based approach *Khan, M. H.; Madiseti, V. K.;* Signals, Systems & Computers, 1998. Conference Record of the Thirty-Second Asilomar Conference on , Volume: 2 , 1-4 Nov. 1998 Pages:1720 - 1724 vol.2
 14. BEEHIVE: an adaptive, distributed, embedded signal processing environment *Famorzadeh, S.; Madiseti, V.; Egoif, T.; Nguyen, T.;* Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997 IEEE International Conference on , Volume: 1 , 21-24 April 1997 Pages:663 - 666 vol.1
 15. Target detection from coregistered visual-thermal-range images *Perez-Jacome, J. E.; Madiseti, V. K.;* Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997 IEEE International Conference on , Volume: 4 , 21-24 April 1997 Pages:2741 - 2744 vol.4
 16. Variable block size adaptive lapped transform-based image coding *Klausutis, T.J.; Madiseti, V. K.;* Image Processing, 1997. Proceedings., International Conference on , Volume: 3 , 26-29 Oct. 1997

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Pages:686 - 689 vol.3

17. A Rate 8/ 10 (0, 6) MTR Code And Its Encoder/decoder *Jaejin Lee; Madiseti, V. K.*; Magnetics Conference, 1997. Digests of INTERMAG '97., 1997 IEEE International , 1-4 April 1997 Pages: BS-15 - BS-15
18. VHDL models supporting a system-level design process: a RASSP approach *DeBardelaben, J.A.; Madiseti, V.K.; Gadiant, A.J.*; VHDL International Users' Forum, 1997. Proceedings , 19-22 Oct. 1997 Pages:183 – 188
19. A performance modeling framework applied to real time infrared search and track processing *Pauer, E.K.; Pettigrew, M.N.; Myers, C.S.; Madiseti, V.K.*; VHDL International Users' Forum, 1997. Proceedings , 19-22 Oct. 1997 Pages:33 – 42
20. Rapid prototyping of DSP systems via system interface module generation *Famorzadeh, S.; Madiseti, V. K.*; Acoustics, Speech, and Signal Processing, 1996. ICASSP-96. Conference Proceedings., 1996 IEEE International Conference on , Volume: 2 , 7-10 May 1996 Pages:1256 - 1259 vol. 2
21. Rapid prototyping of DSP chip-sets via functional reuse *Romdhane, M. S.B.; Madiseti, V. K.*; Acoustics, Speech, and Signal Processing, 1996. ICASSP-96. Conference Proceedings., 1996 IEEE International Conference on , Volume: 2 , 7-10 May 1996 Pages:1236 - 1239 vol. 2
22. A constructive deconvolution procedure of bandpass signals by homomorphic analysis *Marenco, A. L.; Madiseti, V. K.*; Geoscience and Remote Sensing Symposium, 1996. IGARSS '96. 'Remote Sensing for a Sustainable Future.', International , Volume: 3 , 27-31 May 1996 Pages:1592 - 1596 vol.3
23. A parallel mapping of backpropagation algorithm for mesh signal processor *Khan, S.A.; Madiseti, V.K.*; Neural Networks for Signal Processing [1995] V. Proceedings of the 1995 IEEE Workshop , 31 Aug.-2 Sept. 1995 Pages:561 – 570
24. Virtual prototyping of embedded DSP systems *Madiseti, V. K.; Egolf, T.; Famorzadeh, S.; Dung, L. - R.*; Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conference on , Volume: 4 , 9-12 May 1995 Pages:2711 - 2714 vol.4
25. Assessing and improving current practice in the design of application-specific signal processors *Shaw, G.A.; Anderson, J.C.; Madiseti, V.K.*; Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conference on , Volume: 4 , 9-12 May 1995 Pages:2707 - 2710 vol.4
26. Introduction to ARPA's RASSP initiative and education/facilitation program *Corley, J.H.; Madiseti, V.K.; Richards, M.A.*; Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conference on , Volume: 4 , 9-12 May 1995 Pages:2695 - 2698 vol.4
27. DSP design education at Georgia Tech *Madiseti, V. K.; McClellan, J. H.; Barn well, T. P., III*; Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conference on Volume: 5 , 9-12 May 1995 Pages:2869 - 2872 vol.5-+

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28. Yield-based system partitioning strategies for MCM and ASEM design *Khan, S.; Madiseti, V.;* Multi-Chip Module Conference, 1994. MCMC-94, Proceedings., 1994 IEEE, 15-17 March 1994
Pages:144 – 149
 29. Multitrack RLL codes for the storage channel with immunity to intertrack interference Lee, J.;
Madiseti, V.K.; Global Telecommunications Conference, 1994. GLOBECOM '94.,
“Communications: The Global Bridge”., IEEE, Volume: 3 , 28 Nov.-2 Dec. 1994, Pages:1477 - 1481
vol.3
 30. Performance of a fast analog VLSI implementation of the DFT *Buchanan, B.; Madiseti, V.; Brooke,
M.;* Circuits and Systems, 1992., Proceedings of the 35th Midwest Symposium on , 9-12 Aug. 1992
Pages:1353 - 1356 vol.2
 31. Task scheduling in the Georgia Tech digital signal multiprocessor *Curtis, B.A.; Madiseti, V.K.;*
Acoustics, Speech, and Signal Processing, 1992. ICASSP-92., 1992 IEEE International Conference on
, Volume: 5 , 23-26 March 1992 Pages:589 - 592 vol.5
 32. The fast discrete Radon transform *Kelley, B. T.; Madiseti, V. K.;* Acoustics, Speech, and Signal
Processing, 1992. ICASSP-92., 1992 IEEE International Conference on , Volume: 3 , 23-26 March
1992 Pages:409 - 412 vol.3
 33. High speed migration of multidimensional seismic data *Kelley, B.; Madiseti, V.;* Acoustics, Speech,
and Signal Processing, 1991. ICASSP-91., 1991 International Conference on , 14-17 April 1991
Pages:1117 - 1120 vol.2
 34. Efficient distributed simulation *Madiseti, V.; Walrand, J.; Messerschmitt, D.;* Simulation
Symposium, 1989. The 22nd Annual , March 28-31, 1989 Pages:5 - 6
 35. Seismic migration algorithms using the FFT approach on the NCUBE multiprocessor *Madiseti, V.
K.; Messerschmitt, D. G.;* Acoustics, Speech, and Signal Processing, 1988. ICASSP-88., 1988
International Conference on , 11-14 April 1988
 36. Seismic migration algorithms on multiprocessors *Madiseti, V. K.; Messerschmitt, D. G.;* Acoustics,
Speech, and Signal Processing, 1988. ICASSP-88., 1988 International Conference on , 11-14 April
1988 Pages:2124 - 2127 vol.4
 37. WOLF: A rollback algorithm for optimistic distributed simulation systems *Madiseti, V.; Walrand, J.;*
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